Computer Architecture
Quiz 2, 10 Points
Time: 20 min.
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Sinai University
Faculty of Information Technology
& Computer Science
Dr. Ahmed Sallam

الأسم: ----------

1. (5 Points) **True or False?**

a. RISC architecture operates on expensive and complex hardware. [ ]

b. In Load/store architecture, operate instructions operate only on registers. [ ]

c. Uniform Instructions usually are of variable length . [ ]

d. X86 can be considered RISC and CISC at the same time. [ ]

e. Non-uniform instruction are complex to be decoded. [ ]

2. (5 points) **Consider the Instruction (**`Addi Rt,Rs,5`**).**

a. What is the interpretation of this instruction? :

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b. Referring to the previous figure, Which resources (blocks) perform a useful function for this instruction?

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c. Referring to the previous figure, Which resources (blocks) produce no outputs for this instruction?

Do your best, Good Luck