

“This exam in 5 pages, make sure to read all the questions and use the backside as a draft area”

1. (15 Points) True or False? Why?

- a. Nowadays Processors getting faster by increasing the number of cores/transistors. []
.....
- b. GPU are optimized to decrease latency. []
.....
- c. An SM can run more than one block at the same time. []
.....
- d. Accessing Shared memory takes less time compared to accessing local memory. []
.....
- e. Map pattern represent a many to one relation between the input and the output. []
.....
- f. Blurring an image is an application example of Gather pattern. []
.....
- g. Total work complexity can be used alone to measure a parallel program efficiency. []
.....
- h. Parallel Reduce program has steps complexity = $O(\log n)$. []
.....
- i. The factorial operation is a reduction operator. []
.....
- j. Blelloch reduce algorithm is the best solution if we have a massive input size. []
.....
- k. Atomic operations increase the parallel algorithm efficiency. []
.....
- l. It's not possible to write a parallel solution for the Compact problem . []
.....
- m. The total work complexity of Merge sort is $O(n \log n)$. []
.....
- n. Coalesce global memory accesses decrease GPU programming efficiency. []
.....
- o. GPUs can perform loop instructions better than CPUs. []
.....

2. (10 points) Read the following code snippet then answer the questions:

```
1 void my_fun(float * d_out, float * d_in, int size){  
2  
3     for(int i=0;i<size;i++)  
4         d_out[i]=d_in[i]*d_in[i];  
5 }
```

a. What is purpose of this program ?

.....
.....
.....
.....

b. What are the steps and total work complexity of this program?

.....
.....
.....
.....

c. Write a CUDA kernel to parallelize this program:

.....
.....
.....
.....
.....
.....
.....
.....
.....
.....

d. What is the steps and total work complexity of your CUDA program?

.....
.....
.....
.....

