

"This exam in $\underline{5}$ pages, make sure to read all the questions and use the backside as a draft area"

1. (15 Points) True or False? Why?

a.	Nowadays Processors getting faster by increasing the number of cores/transistors.	[]
b.	GPU are optimized to decrease latency.]
c.	An SM can run more than one block at the same time.	[]
d.	Accessing Shared memory takes less time compared to accessing local memory.	[]
e.	Map pattern represent a many to one relation between the input and the output.	[]
f.	Blurring an image is an application example of Gather pattern.	[]
g.	Total work complexity can be used alone to measure a parallel program efficiency.	[]
h.	Parallel Reduce program has steps complexity = O(logn).	[]
i.	The factorial operation is a reduction operator.	[]
j.	Blelloch reduce algorithm is the best solution if we have a massive input size.	[]
k.	Atomic operations increase the parallel algorithm efficiency.	[]
I.	It's not possible to write a parallel solution for the Compact problem .	[]
m.	The total work complexity of Merge sore is O(nlogn).	[]
n.	Coalesce global memory accesses decrease GPU programming efficiency.	[]
О.	GPUs can perform loop instructions better than CPUs.	[]



2. (10 points) Read the following code snippet then answer the questions:

1	<pre>void my_fun(float * d_out, float * d_in, int size){</pre>
2	
3	for(int i=0;i <size;i++)< th=""></size;i++)<>
4	<pre>d out[i]=d in[i]*d in[i];</pre>
5]

1	}
a.	What is purpose of this program ?
b.	What are the steps and total work complexity of this program?
C	Write a CUDA kernel to parallelize this program:
d.	What is the steps and total work complexity of your CUDA program?

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3.	(15 points	Use the f	ollowing	array to	answer	the a	uestions:	[2.	1.	7. 5	5. 0). 3	. 4	. 1.	0.	. 6	1:

Apply an exclusive max scan on the given array and show every step:
Given list of segments heads [1, 0, 0, 0, 1, 0, 0, 1]. Apply segmented sum scan (Inclusive) technic
Apply the Radix sort, and show every step:
Apply the Radix sort, and show every step:
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Apply the Radix sort, and show every step:
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- 4. (10 points) Given the following Matrices multiplication $\begin{bmatrix} 0 & 1 & 2 \\ 5 & 0 & 3 \\ 0 & 3 & 0 \end{bmatrix} \times \begin{bmatrix} 4 \\ 1 \\ 0 \end{bmatrix}$.
 - a. What is the total work to multiply these matrices?
 - b. Convert the matrices to CPR format and apply the multiplication (show every step).
 - c. What is the total work to multiply these matrices using CPR format?

d.	Explain how can we program CPR multiplication in parallel?



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	l operations take		how long to co	mplete the follow	ving:	
cudaS	loperations take caream_t s1, s MemcpyAsync(11, 128, s2 >	2; &d_arr1, &h_				
cudaS	tream_t s1, s MemcpyAsync(2; &d_arr1, &h_				
cudaS	tream_t s1, s MemcpyAsync(2; &d_arr1, &h_				
cudaS	tream_t s1, s MemcpyAsync(2; &d_arr1, &h_				
cudaS	tream_t s1, s MemcpyAsync(2; &d_arr1, &h_				
cudaS	tream_t s1, s MemcpyAsync(2; &d_arr1, &h_				
cudaS- a. cuda A<<<	tream_t s1, s MemcpyAsync(2; &d_arr1, &h_ >>(d_arr2); 	_arr1, numb	ytes, cudaH2	D, s1)	
cudaS- a. cuda A<<<	MemcpyAsync(1, 128, s2 >	2; &d_arr1, &h_ >>(d_arr2); 	_arr1, numb	ytes, cudaH2	D, s1)	
cudaS- a. cuda A<<<	MemcpyAsync(1, 128, s2 >	2; &d_arr1, &h_ >>(d_arr2); 	_arr1, numb	ytes, cudaH2	D, s1)	